

Form PTO 18-49
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APPL-P2828COASerial No.
10/607,736

Information Disclosure Statement by Applicant

Applicant: Erik P. Staats

(Use several sheets if necessary)

Filed: June 26, 2003 Group: 2182

U.S. Patent Documents

Init.		Document No.	Date	Name	Class	Subclass	Filing Date

Foreign Documents

Translation

Init.		Document No.	Date	Country	Class	Subclass	Yes	No
	1	EP 1 085 706 A2	03/21/2001	Europe	H04L	12/46		X
	2	EP 1 085 706 A3	10/30/2002	Europe	H04L	12/46		X

Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

	3	Bregni et al., "Jitter Testing Technique and Results at VC-4 Desynchronizer Output of SDH Equipment, <u>IEEE International Conference on Communications</u> , Vol. 3, pp. 1407-1410, May 12, 1994.
	4	"Information technology-Microprocessor systems--Control and Status Registers (CSR) Architecture for microcomputer buses", ANSI/IEEE Standard 1212, The Institute of Electrical and Electronics Engineers, Inc. pp. 1-122, 1994 Edition.
	5	Bregni et al., "Jitter Testing Technique and Results at VC-4 Desynchronizer Output of SDH Equipment, <u>IEEE Transactions on Instrumentation and Measurement</u> , Vol. 44, Issue 3, pp. 675-678, June 1995.
	6	"IEEE Standard for a High Performance Serial Bus", IEEE Standard 1394-1995, Institute of Electrical and Electronics Engineers, Inc., pp. 1-384, approved July 22, 1996.
	7	Shiwen et al., "Parallel Positive Justification in SDH C ₄ Mapping, <u>IEEE International Conference on Communications</u> , Vol. 3, pp. 1577-1581, June 12, 1997
	8	"AV/C Digital Interface Command Set General Specification, Rev. 3.0", 1394 Trade Association, pp. 4-5, 20-34, April 15, 1998.
	9	"Enhancements to the AV/C General Specification 3.0 Version 1.0FC1", 1394 Trade Association, pp. 4, 6-17, November 5, 1998.
	10	"Information Technology-Fibre Channel-Methodologies for Jitter Specification", NCITS TR-25-1999, Jitter Working Group Technical Report, Rev. 10, pp. 1-96, June 9, 1999.
	11	"P1394a Draft Standard for a High Performance Serial Bus (Supplement)", Draft 3.0, Institute of Electrical and Electronics Engineers, Inc., pp. 1-187, June 30, 1999.
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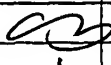
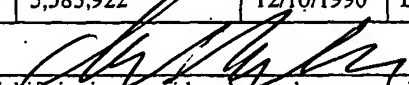
Examiner

Date Considered

2-21-05

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Form PTO 1449 (Rev. 2-32) U.S. Department of Commerce Patent and Trademark Office				Atty. Docket No. ZAY-99-041		Serial No. 09/432,872	
Information Disclosure Statement by Applicant				Applicant: Erik P. Staats			
(Use several sheets if necessary)				Filed: November 2, 1999 Group: 2182			
U.S. Patent Documents							
Init.		Document No.	Date	Name	Class	Subclass	Filing Date
<i>ca</i>	1	4,156,798	05/29/1979	Doelz	179	15 AL	08/29/1977
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Foreign Documents							
Init.		Document No.	Date	Country	Class	Subclass	Translation Yes No
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61		"IEEE Standard for a High Performance Serial Bus", IEEE Standard 1394-1995, Institute of Electrical and Electronics Engineers, Inc., August 30, 1996.					
62		"IEEE Standard for a High Performance Serial Bus-Amendment 1", Institute of Electrical and Electronics Engineers, Inc., pp. 1-196, 2000 (no month).					
63		"AV/C Digital Interface Command Set General Specification, Rev. 3.0", 1394 Trade Association, pp. 4-5, 20-34, April 15, 1998.					
64		"Enhancements to the AV/C General Specification 3.0 Version 1.0FC1", 1394 Trade Association, pp. 4, 6-17, November 5, 1998.					
65		"Fibre Channel-Methodologies for Jitter Specification", NCITS TR-25-1999, Jitter Working Group Technical Report, Rev. 10, pp. 1-96, June 9, 1999.					
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